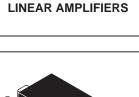
# The RF Line UHF Linear Amplifier

Designed for linear amplifier applications in 50 Ohm systems requiring wide bandwidth, low noise, and low distortion. Internal DC blocking on RF ports reduces external component count and related circuit area. This hybrid utilizes push–pull circuit design.

• Supply Voltage: 15 Vdc

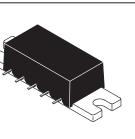
- Third Order Intercept: 41.5 dBm Typ
- Power Gain: 17.5 dB Typ (@ 900 MHz)
- Excellent Phase Linearity and Group Delay Characteristics
- 50 Ohm Input/Output Impedances



1 W, 17.5 dB

50-1000 MHz

**MHL8115** 



CASE 448-02, STYLE 2

#### ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub> = 25°C unless otherwise noted)

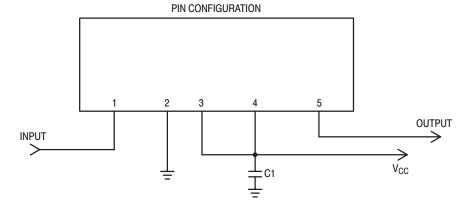
Rating	Symbol	Value	Unit
DC Supply Voltage	V <sub>CC</sub>	18	Vdc
RF Input Power	P <sub>in</sub>	+20	dBm
Storage Temperature Range	T <sub>stg</sub>	-40 to +100	°C
Operating Case Temperature Range	T <sub>C</sub>	-20 to +100	°C

### ELECTRICAL CHARACTERISTICS (T<sub>C</sub> = +25°C; V<sub>CC</sub> = 15 Vdc; 50 Ω System)

Character	istic	Symbol	Min	Тур	Max	Unit
Supply Current		I <sub>DC</sub>		700	760	mA
Power Gain	(f = 900 MHz)	P <sub>G</sub>	16.5	17.5	—	dB
Gain Flatness	(f = 50–1000 MHz)	FL		1.0	2.0	dB
Power Output @ 1 dB Comp.	(f = 900 MHz)	P <sub>out</sub> 1 dB	29	30	—	dBm
Third Order Intercept (f1 = 879 MHz, f2	= 884 MHz)	ITO	40.5	41.5	—	dBm
Input/Output VSWR	(f = 50–900 MHz) (f = 900–1000 MHz)	VSWR	_	_	2.0:1 2.6:1	
Noise Figure, Broadband	(f = 500 MHz) (f = 1000 MHz)	NF	_	7.5 8.5	8.5 9.5	dB
Second Harmonic Distortion ( $P_0 = 100 \text{ r}$	nW, f <sub>2H</sub> = 1000 MHz)	dso		-55	-45	dB
Second Order Intermodulation Distortion ( $P_0 = 2.75$ dBm, $f_1 = 373$ MHz, $f_2 = 45$		IM2	-	-65	-60	dB
Intermodulation Distortion, 3 Tone (f = 8	60 MHz, P <sub>sync</sub> = 200 mW)	IM3	_	-60	_	dB



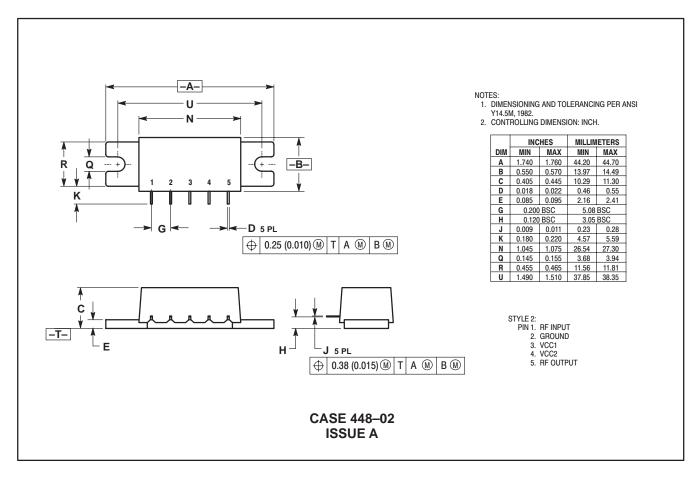
REV 2



 $\text{C1} \geq \text{0.01} \ \mu\text{F} \ \text{(CHIP)}$ 

Figure 1. MHL8115 External Connections

## PACKAGE DIMENSIONS



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